

**ABSTRACT**

The invention relates to a device detecting a demodulated signal received by a spread spectrum receiver and converted into digital samples. The device is characterized by comprising a matched filter for calculating the correlation  
5 between an incoming signal and at least one reference signal; an oscillator for generating a sampling frequency; a sampling circuit for re-sampling said de-modulated digital sample signal at said sampling frequency, which is such that the timing of samples of the reference signals of the matched filter corresponds to the timing of a sample signal going from the sampling circuit to the  
10 matched filter; and a multiplier in which the sample signal is multiplied by a carrier replica generated locally before the sampling circuit or thereafter, to remove the carrier from the sample signal.

(Figure 4)